Abstracts

A Miniaturized, Broadband MMIC Frequency Doubler

T. Hiraoka, T. Tokumitsu and M. Akaike. "A Miniaturized, Broadband MMIC Frequency Doubler." 1990 MTT-S International Microwave Symposium Digest 90.2 (1990 Vol. II [MWSYM]): 819-822.

A miniaturized, broadband brdanced MMIC frequency doubler, which consists of a common gate FET and a common source FET directly connected to each drain elect rode, is proposed. A chip size of 0.5 mm x 0.5 mm is achieved excluding the output matching circuit with conversion loss less than 9 dB, fundamental signal suppression better than 18 dB, and input return loss better than 8 dB from 6 GHz to 16 GHz.

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